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U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10053572	01/24/2002	438		2812	

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**CONTINUING DATA VERIFIED:

** FOREIGN APPLICATIONS VERIFIED:

JAPAN 2001-019293 01/26/2001

PG-PUB	DO NOT PUBLISH	<input type="checkbox"/>	RESCIND	<input type="checkbox"/>	ATTORNEY DOCKET NO
Foreign priority claimed		<input type="checkbox"/> yes <input type="checkbox"/> no			
35 USC 119 conditions met		<input type="checkbox"/> yes <input type="checkbox"/> no	740756-2422		
Verified and Acknowledged Examiner's Initials					
TITLE : Method of manufacturing semiconductor device					
U.S. DEPT. OF COMM (PAT & TM) PTO-450 (Rev. 10-94)					

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED		
		Assistant Examiner		Total Claims Print Claim for O.G
				DRAWING
				Sheets Drwg. Figs. Drwg. Print Fig.
ISSUE FEE				
Amount Due	Date Paid			
<input type="checkbox"/> TERMINAL DISCLAIMER		Primary Examiner		
		PREPARED FOR ISSUE		
		Application Examiner		
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